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ELECTRICAL CIRCUITS LABORATORY LAB MANUAL

ELECTRIC CIRCUITS LABORATORY MANUAL (ECE-235 LAB) GUIDE LINES FOR THE ... not copy or repeat the procedure description from the lab manual. Report the measurement and other ... tables. (e) Analysis of experimental data: Analyze the data. Compare with theoretical results. Produce graphs using MATLAB or MS-Excel and embed the graph figure ...

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3 Introduction General Circuit Analysis I covers DC analysis, transient analysis, and AC analysis. The laboratory exercises begin with an introduction of simulation software (LTspice) to be used both in the labs and in lectures. Lab exercises continue with resistive circuits and powerful analysis techniques, such as nodal analysis, mesh analysis, superposition, source transformation, Thévenin ...

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EE 391 CIRCUIT THEORY LAB. MANUAL EE 391 Page | 5 Example 1: To simulate and study the transient response of a series R-C circuit using MATLAB where $R=200\Omega$, $C=10\mu F$ for the following conditions: 1) source voltage is 40V DC with all initial conditions set equal to zero. 2) source voltage is a pulse signal with a period of 0s, width of 5ms, rise and fall

LABORATORY INSTRUCTION MANUAL

EE 233 Lab 1: RC Circuits Laboratory Manual Page 6 of 11 4 Experimental Procedure and Data Analysis 4.1 The RC Response to a DC Input 4.1.1 Square Wave Input Analysis Build the circuit in Figure 4.1.1 and set the function generator to provide a square wave input as follows: a) The period $T \geq 5\tau$ (to ensure that $V_C \gg 0$).

EE 233 Circuit Theory Lab 1: RC Circuits

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ELECTRICAL CIRCUITS SIMULATION LAB - ASTI

EEL 3552C Lab Manual 1. EXPERIMENT: SPECTRUM ANALYSIS 1.4 Pre-lab Questions: a. Calculate the amplitude in dBV of a 4 KHz, 2 volt peak-to-peak, sine wave. b. Calculate the peak-to-peak voltage of a -10 dBV, 4 KHz, sine wave. 1.5 Procedure: 1.5.1 Time Domain a. Set the Function Generator for a sine wave output with a frequency of 4 KHz.

EEL3552C Lab Manual - ECE Department

SYLLABUS FOR NETWORK ANALYSIS & SYNTHESIS LAB NEC -351: NETWORK ANALYSIS & SYNTHESIS LAB 1. Verification of principle of superposition with dc and ac sources. 2. Verification of Thevenin, Norton and Maximum power transfer theorems in ac circuits 3. Verification of Tellegen's theorem for two networks of the same topology. 4.

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